

WEST Search History

DATE: Friday, July 30, 2004

| Hide? | Set Name | Query | Hit Count |
|--------------------------|-------------|--|--------------|
| | | <i>DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=ADJ</i> | |
| <input type="checkbox"/> | L9 | L8 and l6 | 0 |
| <input type="checkbox"/> | L8 | Texas Instruments | 35716 |
| <input type="checkbox"/> | L7 | L6 and l3 | 0 |
| <input type="checkbox"/> | L6 | (wafer or substrate or semiconductor) same (clean\$3 or rins\$3 or wash\$3 or treat\$3) same \$3sonic\$5 same (laps\$3 or soak\$3 or delay\$3) | 60 |
| | | <i>DB=PGPB,USPT,USOC; PLUR=YES; OP=ADJ</i> | |
| <input type="checkbox"/> | L5 | L3 and L1 | 0 |
| | | <i>DB=EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=ADJ</i> | |
| <input type="checkbox"/> | L4 | L3 and L1 | 0 |
| <input type="checkbox"/> | L3 | 134/1.3,2,3,26,28,34,41;438/906.ccls. | 882 |
| <input type="checkbox"/> | L2 | (wafer or substrate or semiconductor) with (clean\$3 or rins\$3 or wash\$3 or treat\$3) with \$3sonic\$5 with (laps\$3 or soak\$3 or delay\$3) | 1 |
| | | <i>DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=ADJ</i> | |
| <input type="checkbox"/> | L1 | (wafer or substrate or semiconductor) with (clean\$3 or rins\$3 or wash\$3 or treat\$3) with \$3sonic\$5 with (laps\$3 or soak\$3 or delay\$3) | 11 |

END OF SEARCH HISTORY